

FIG. 1

FIG. 2

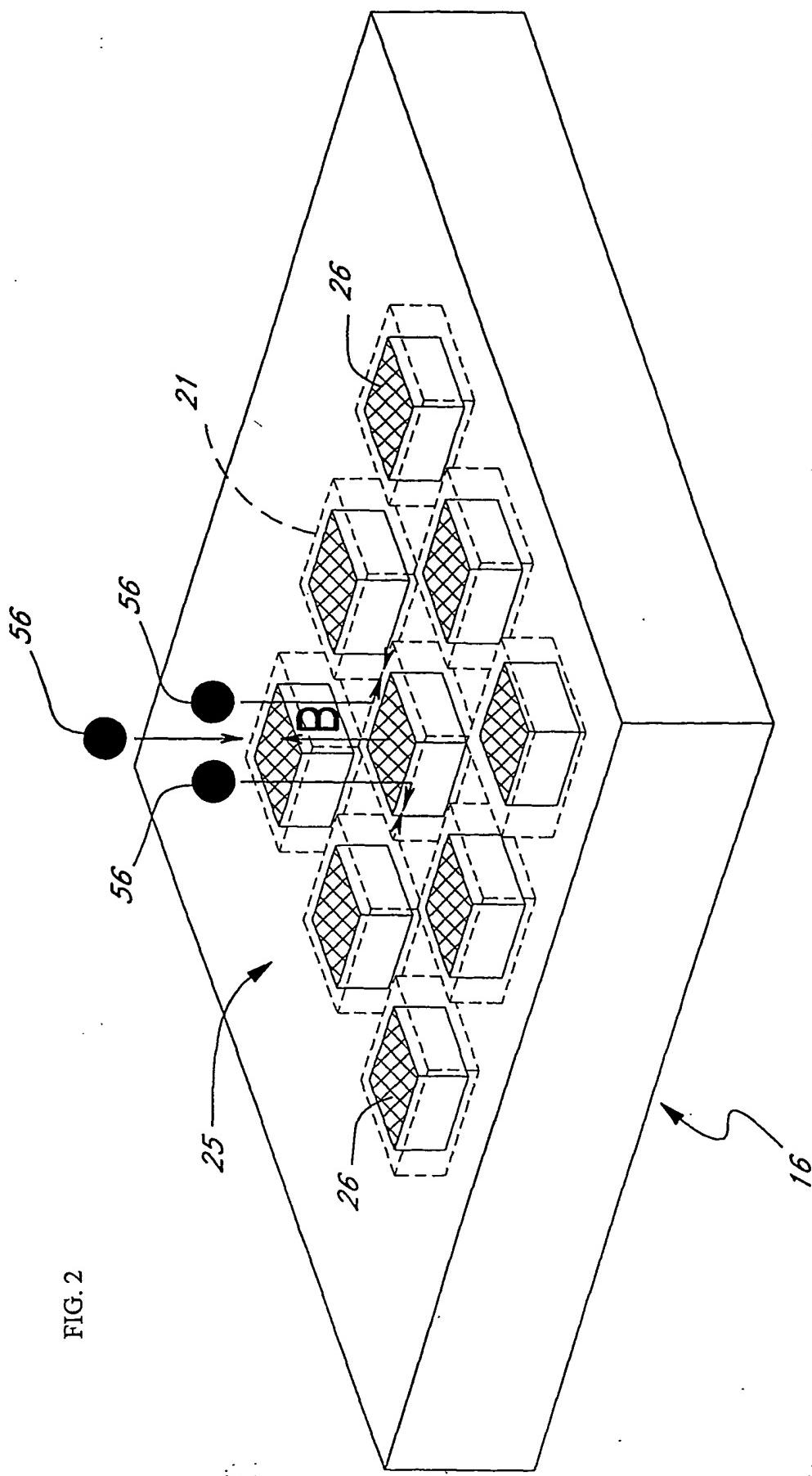


FIG. 3

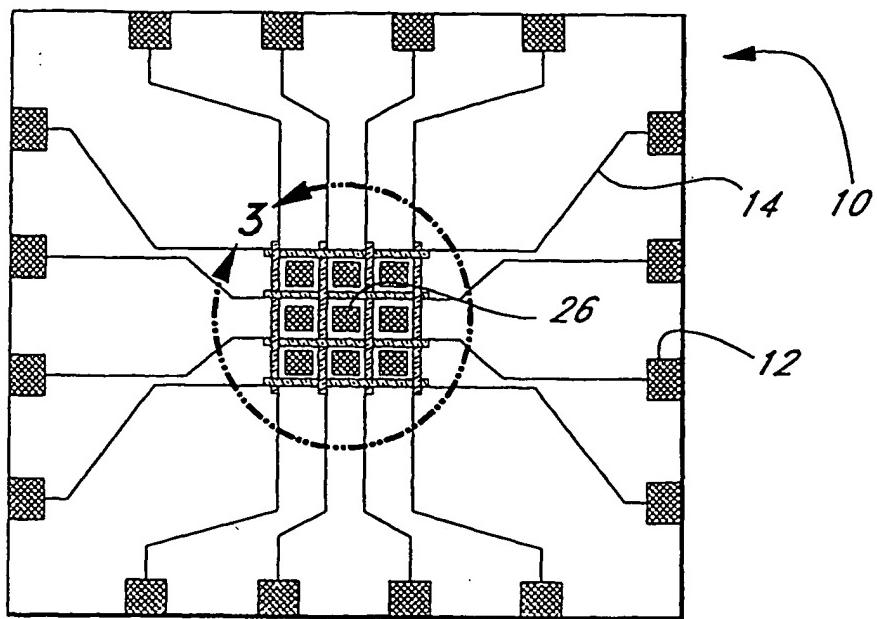


FIG. 4

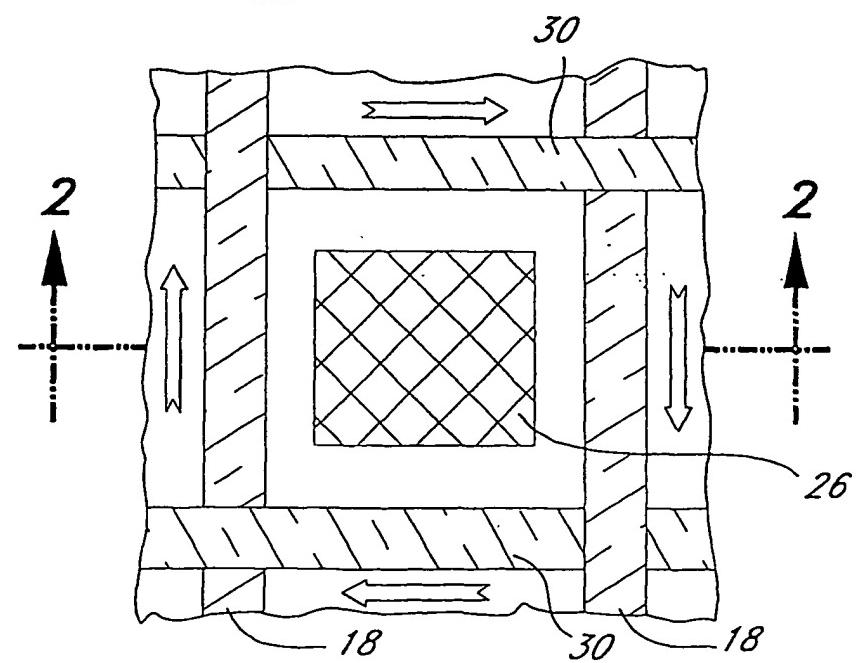
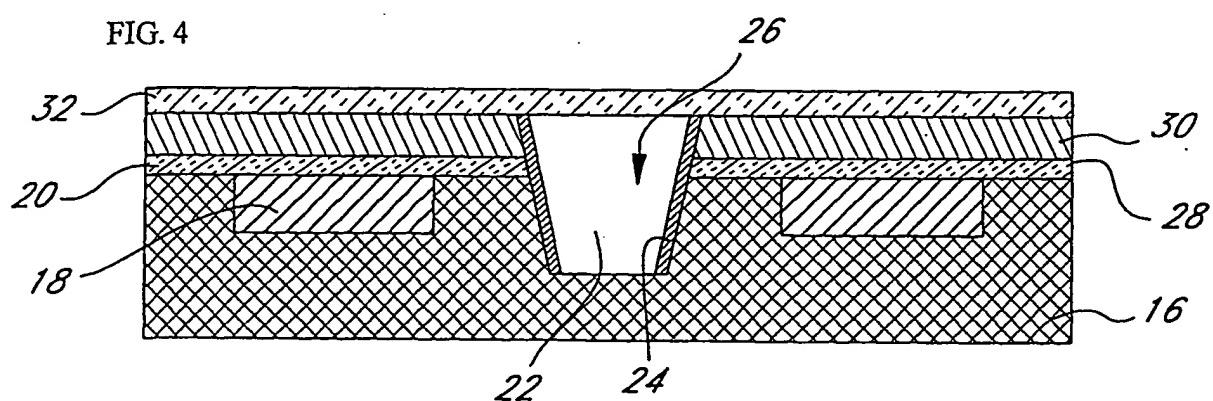


FIG. 5

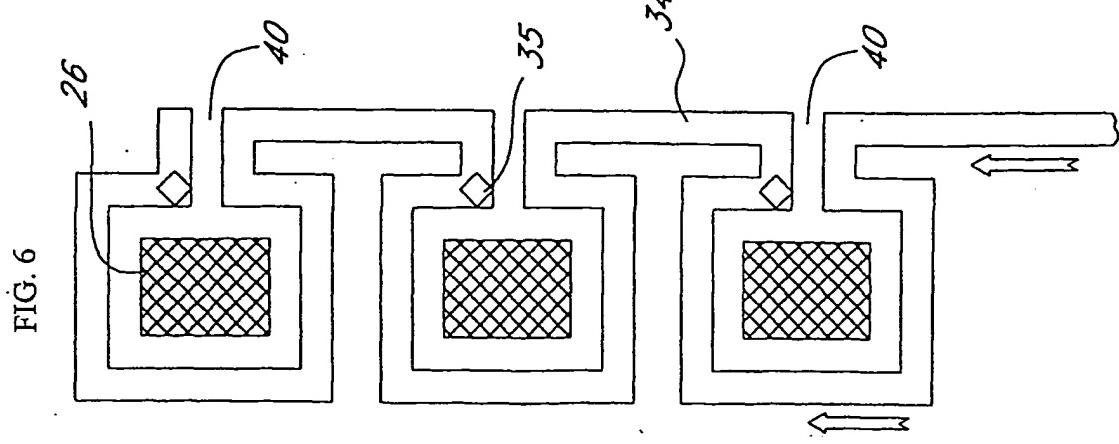
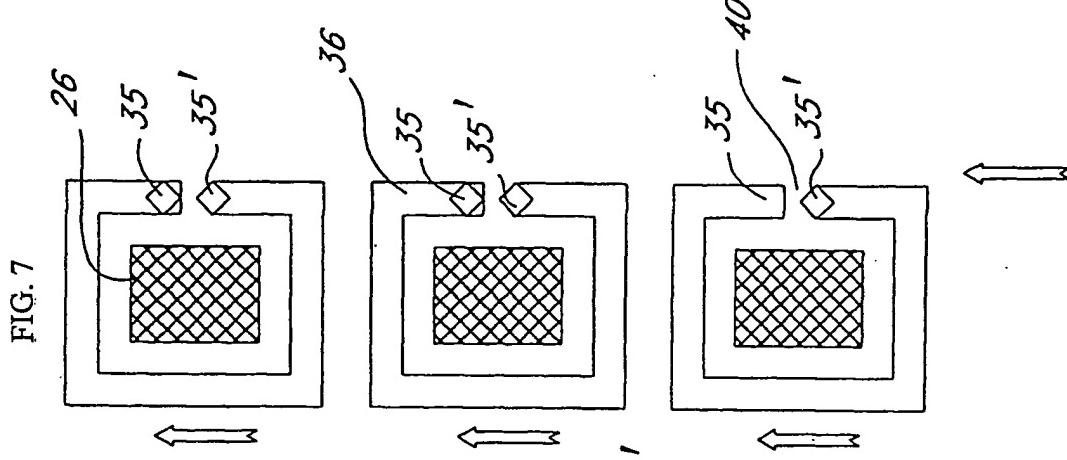
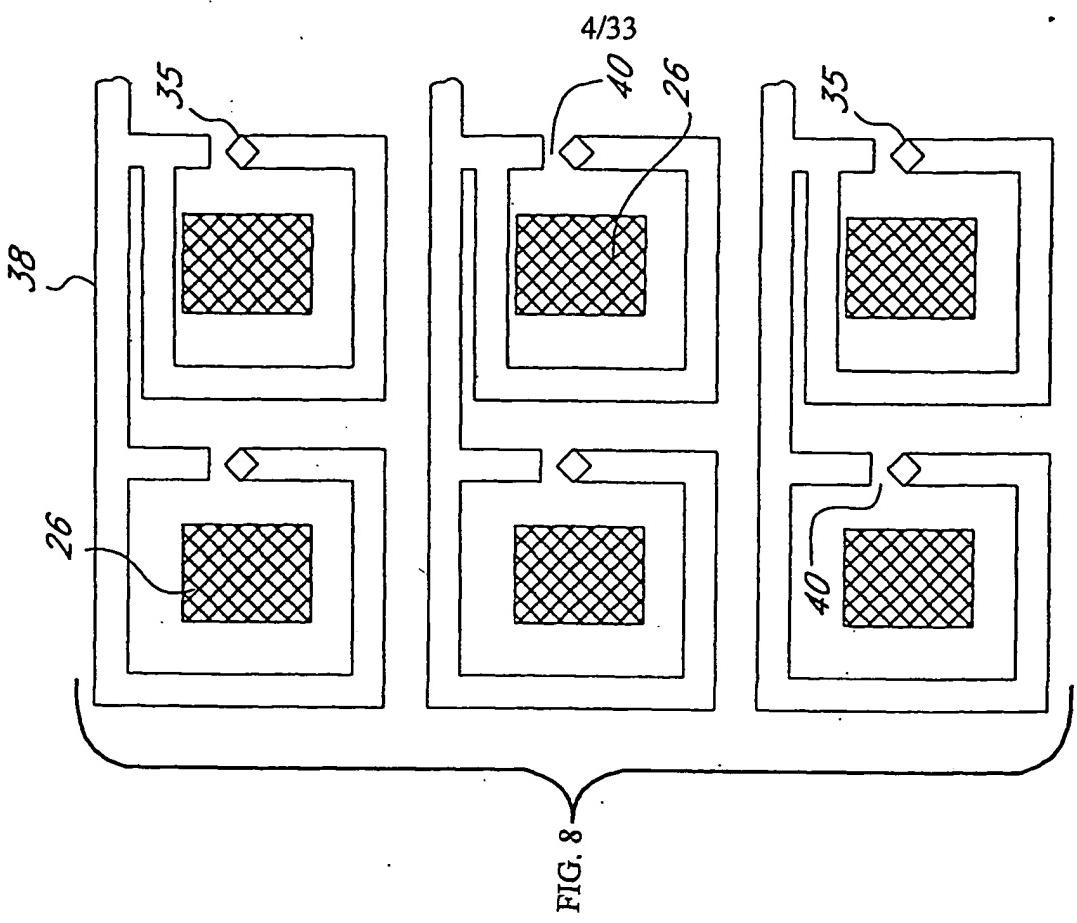


FIG. 9

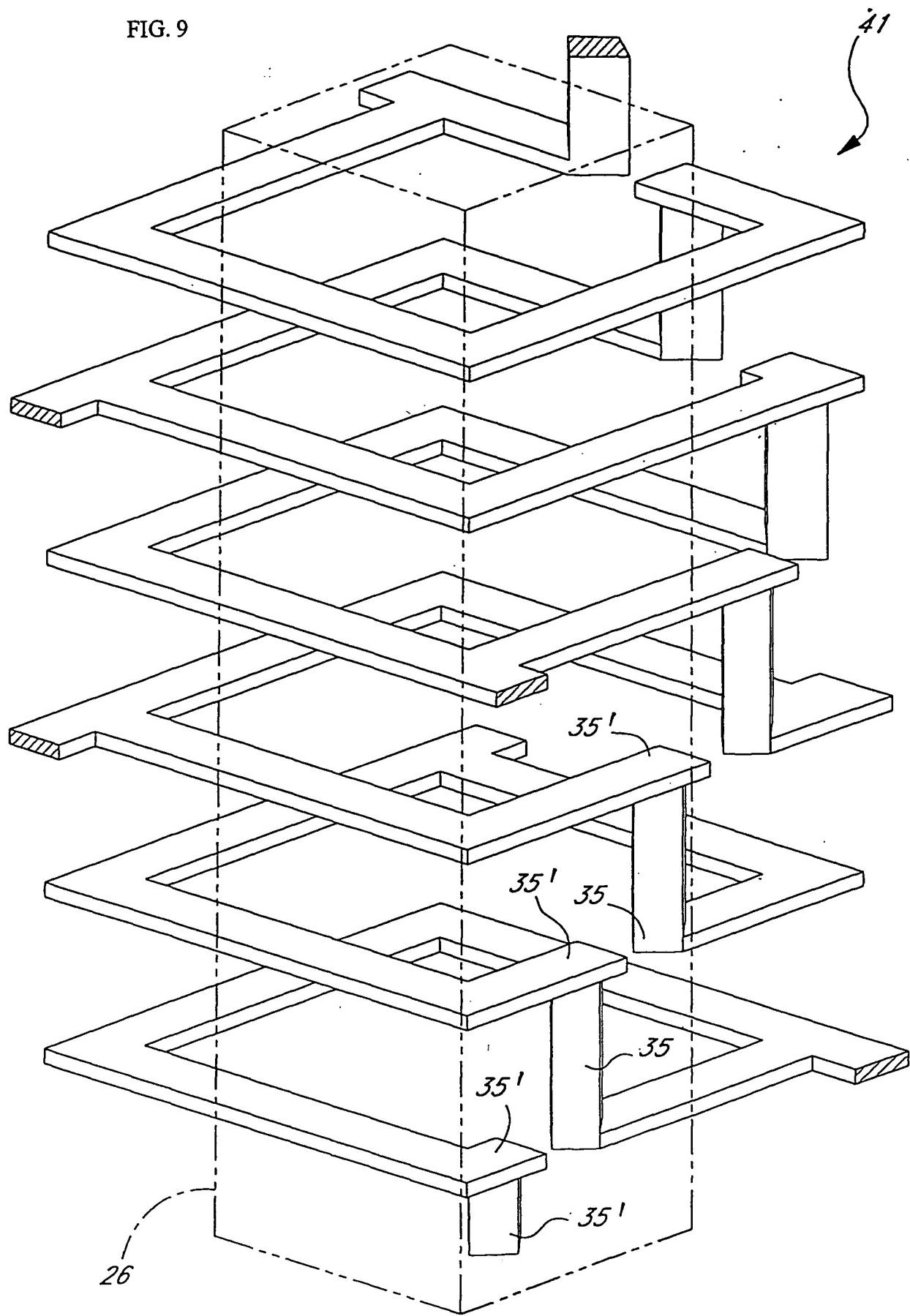


FIG. 10

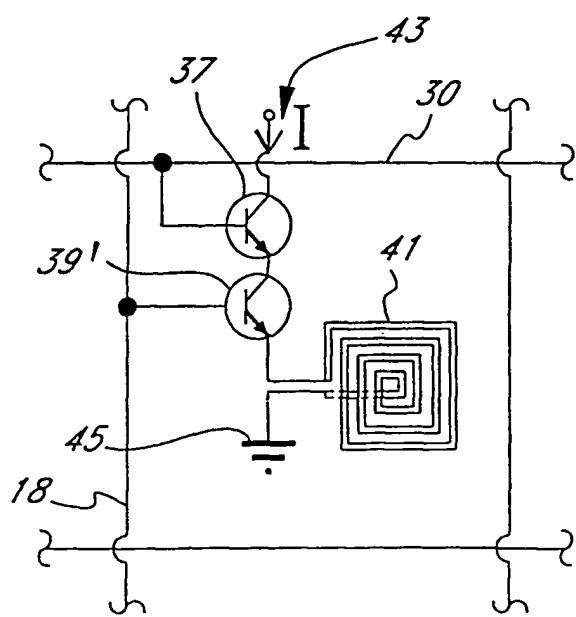
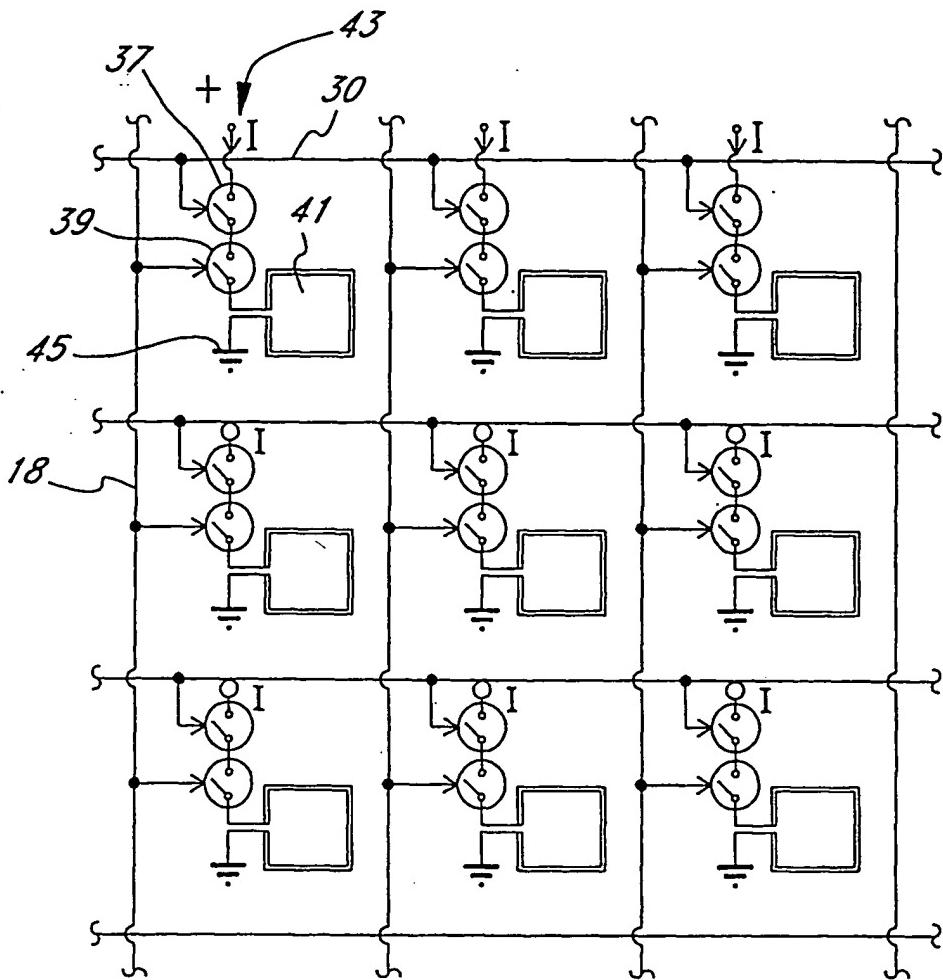


FIG. 11A

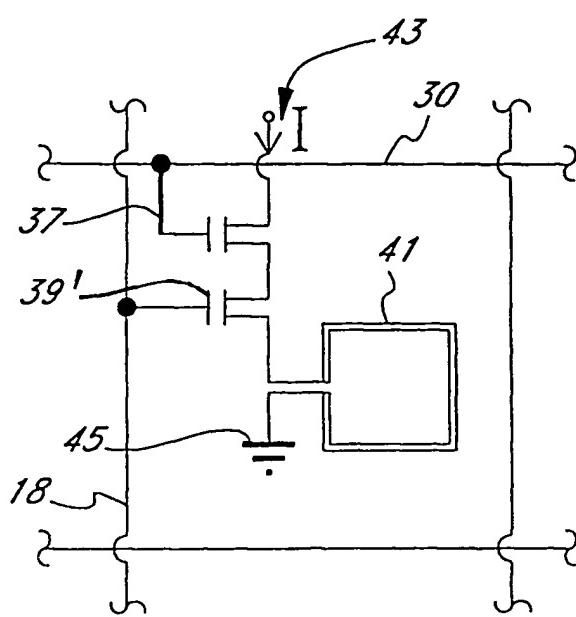


FIG. 11B

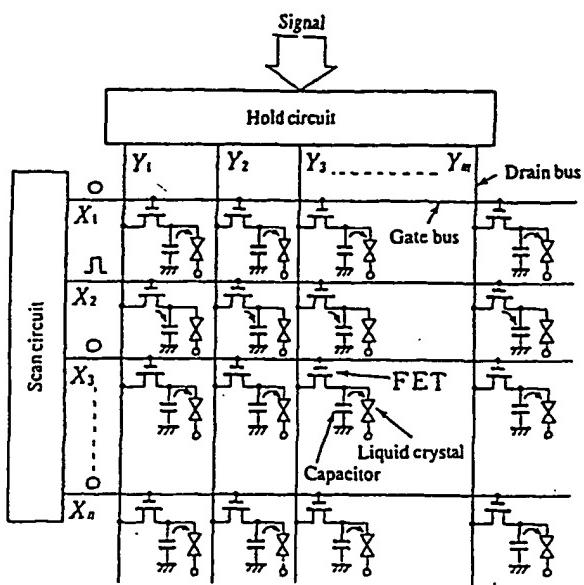


FIG. 11C

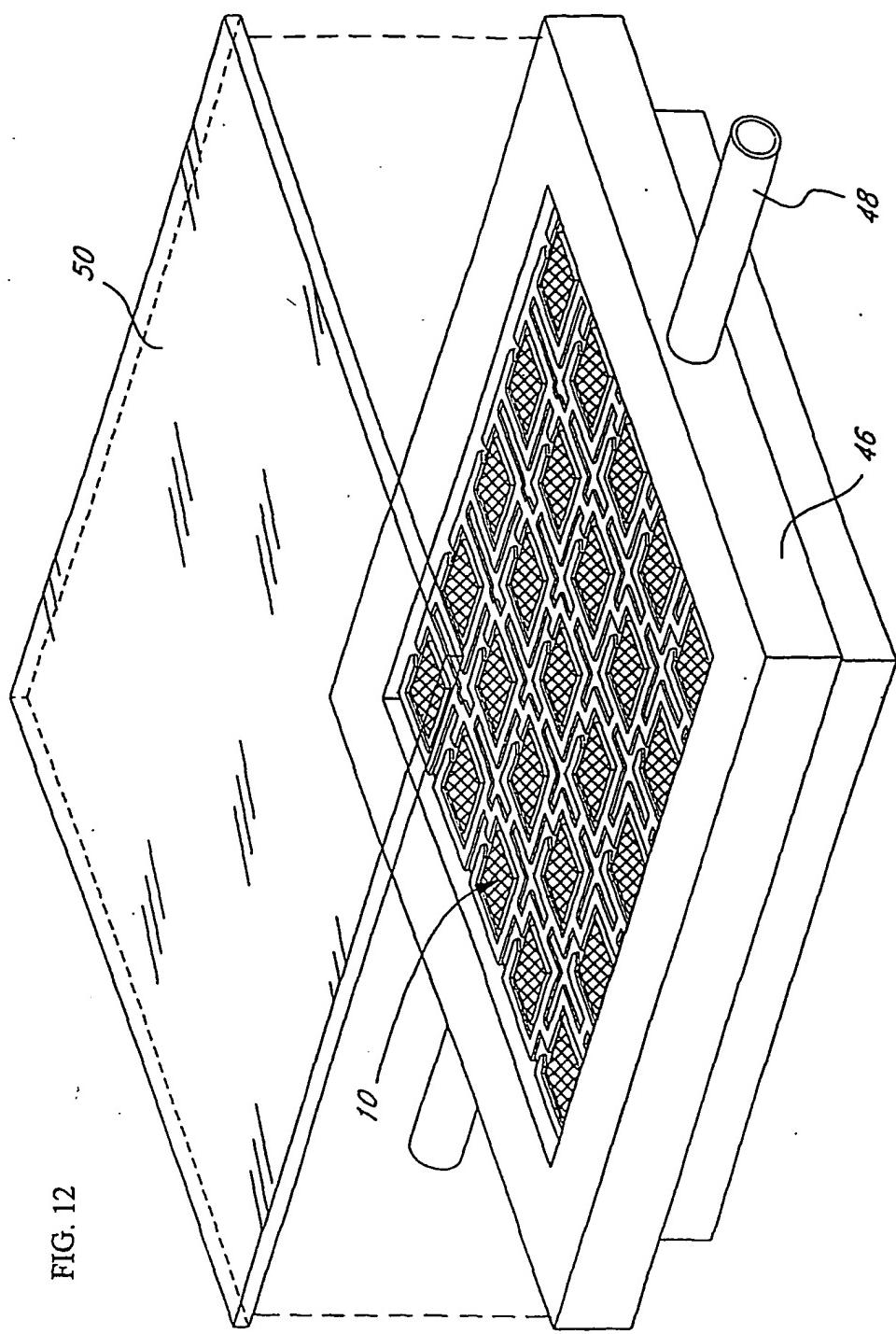
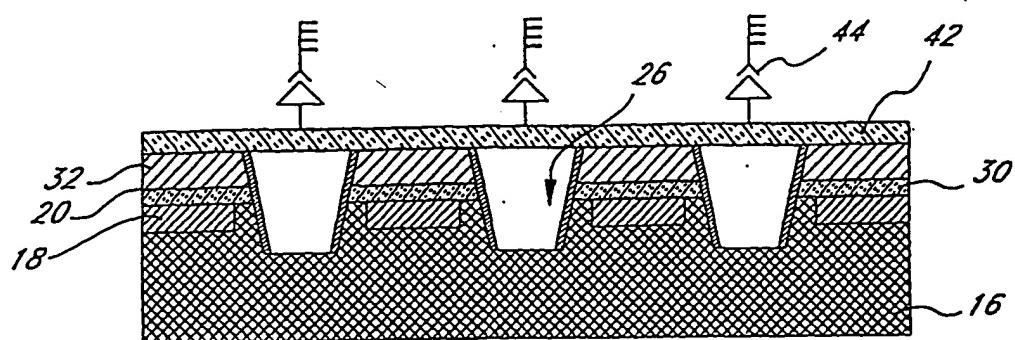


FIG. 12

FIG. 13



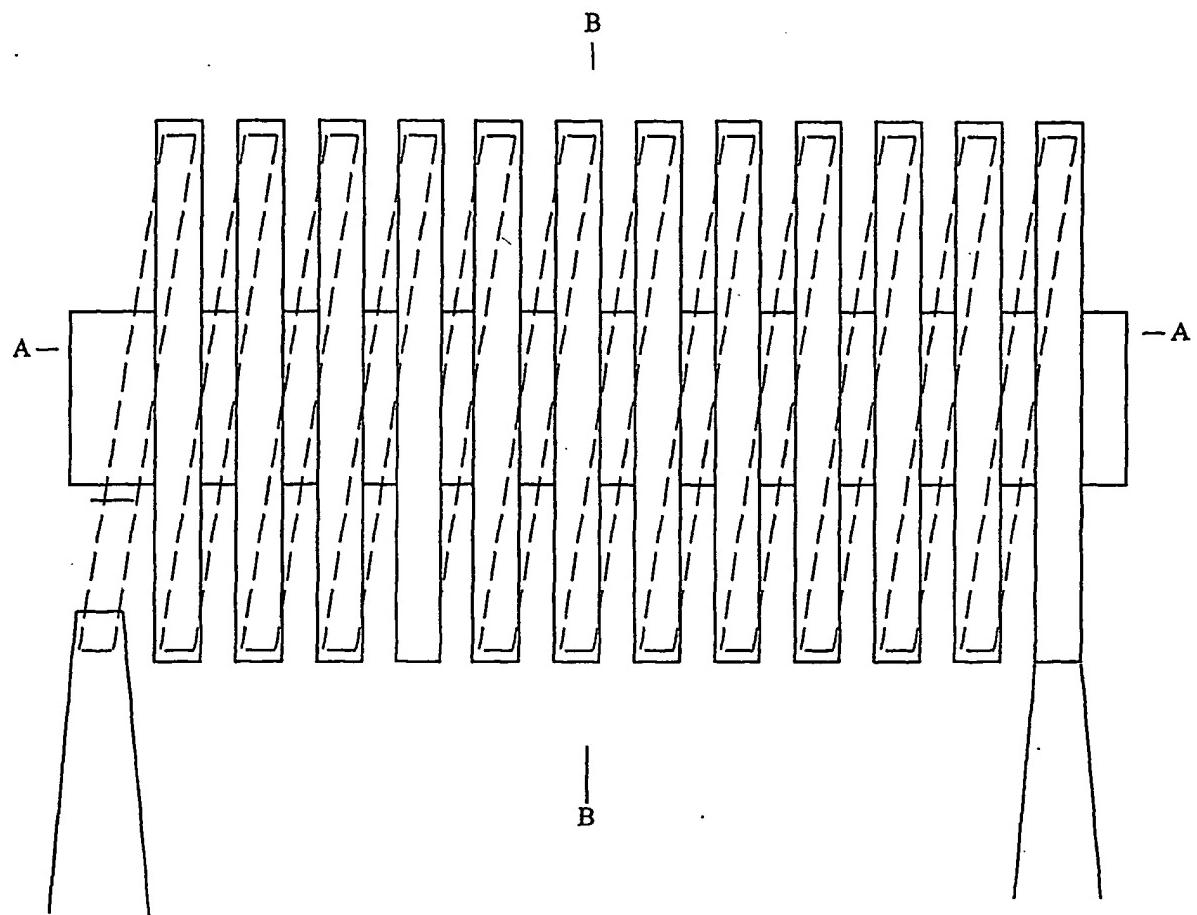


FIG. 14A

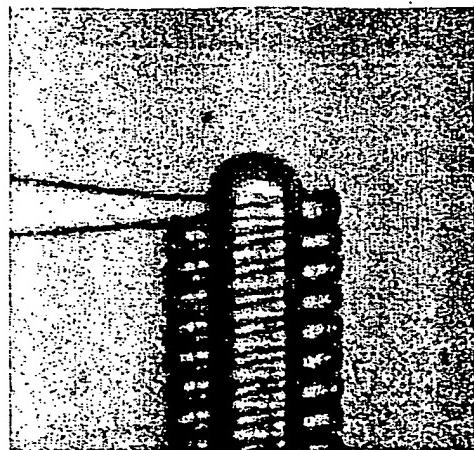


Fig. 14 B

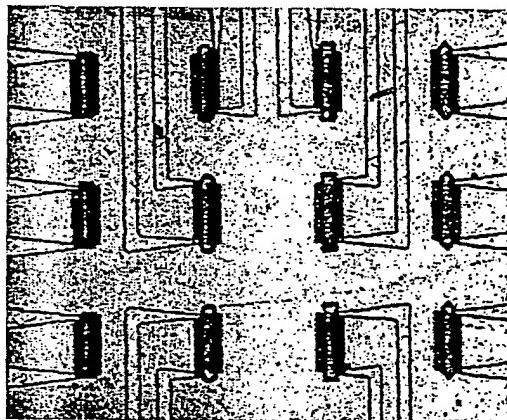


Fig. 14 C

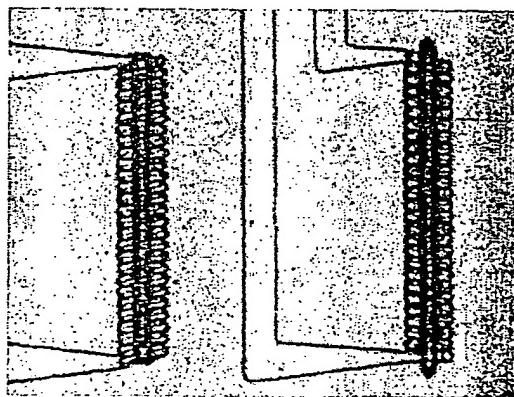


Fig. 14 D

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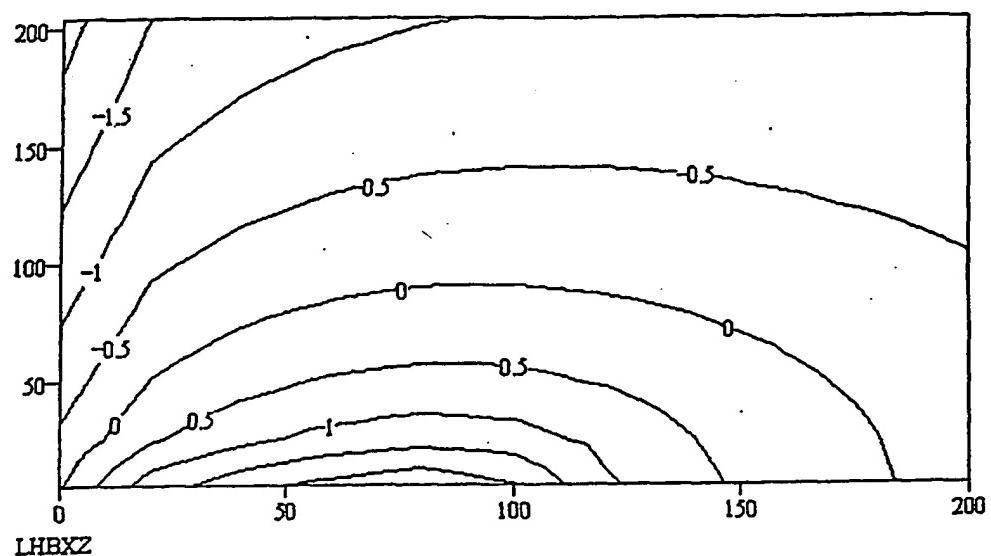


FIG. 15

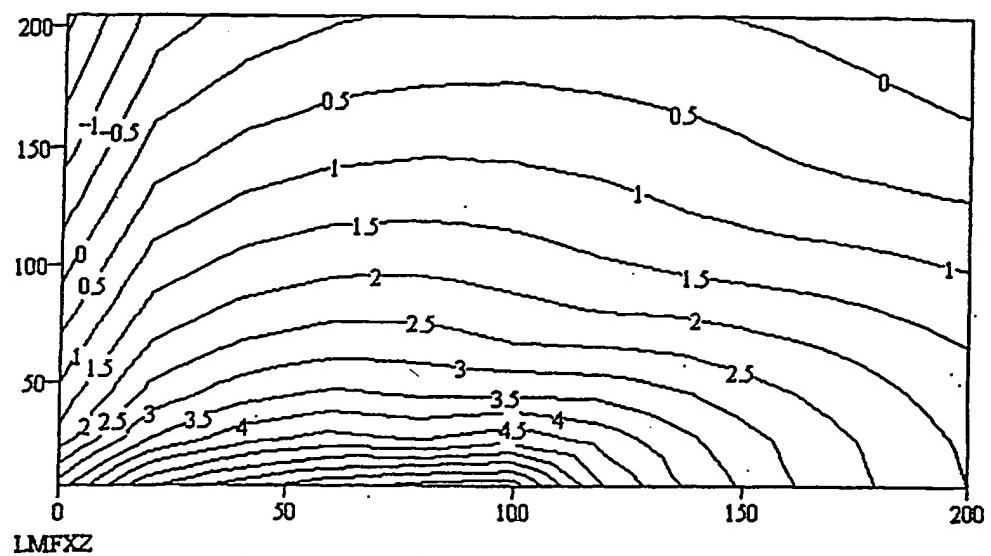


FIG. 16

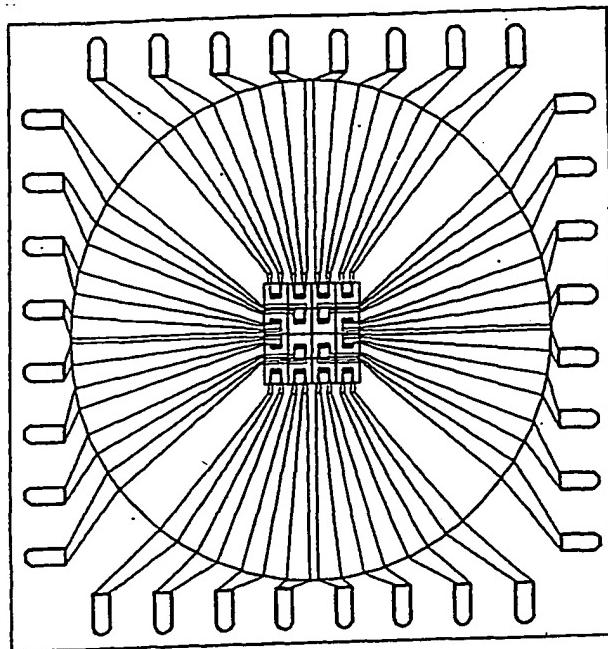


FIG. 17

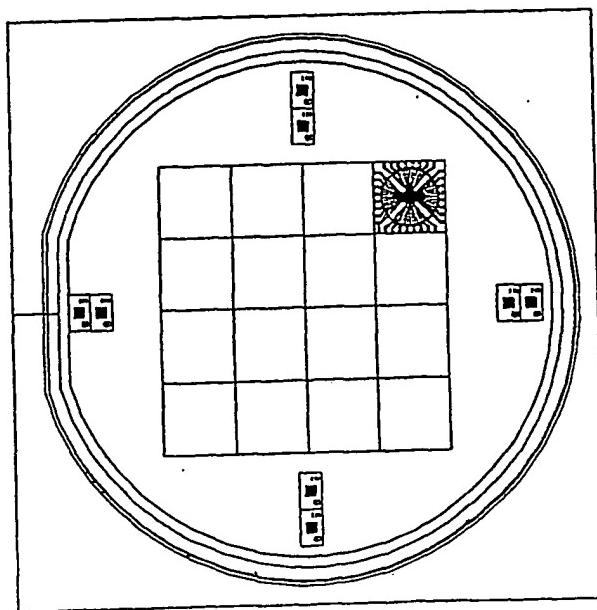


FIG. 18

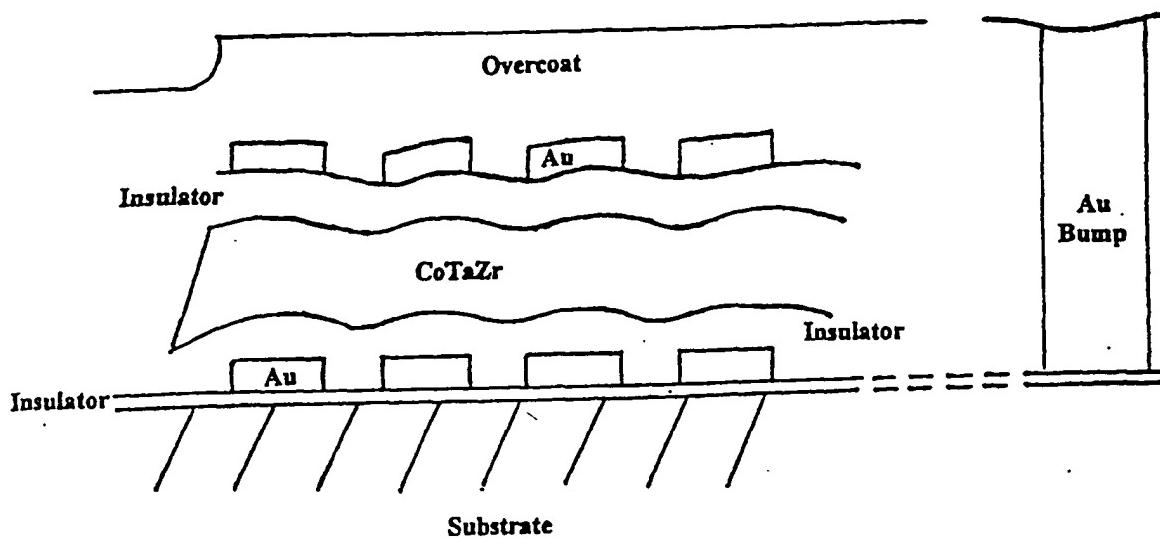


FIG. 19A

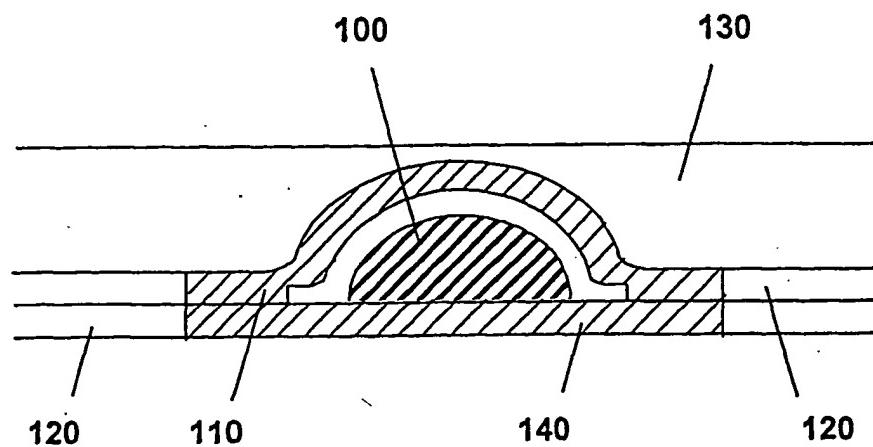


FIG. 19B

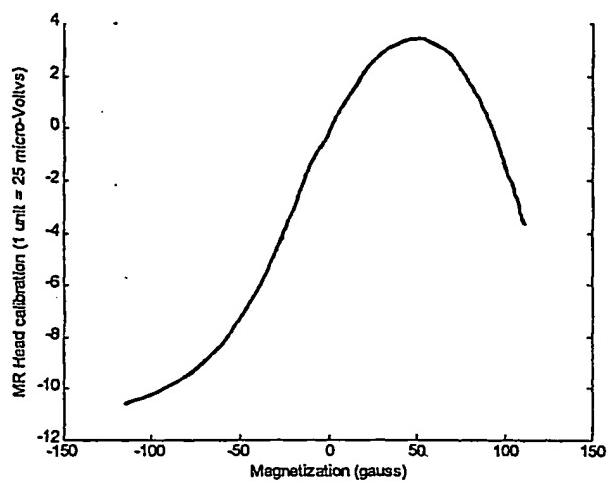


FIG. 20

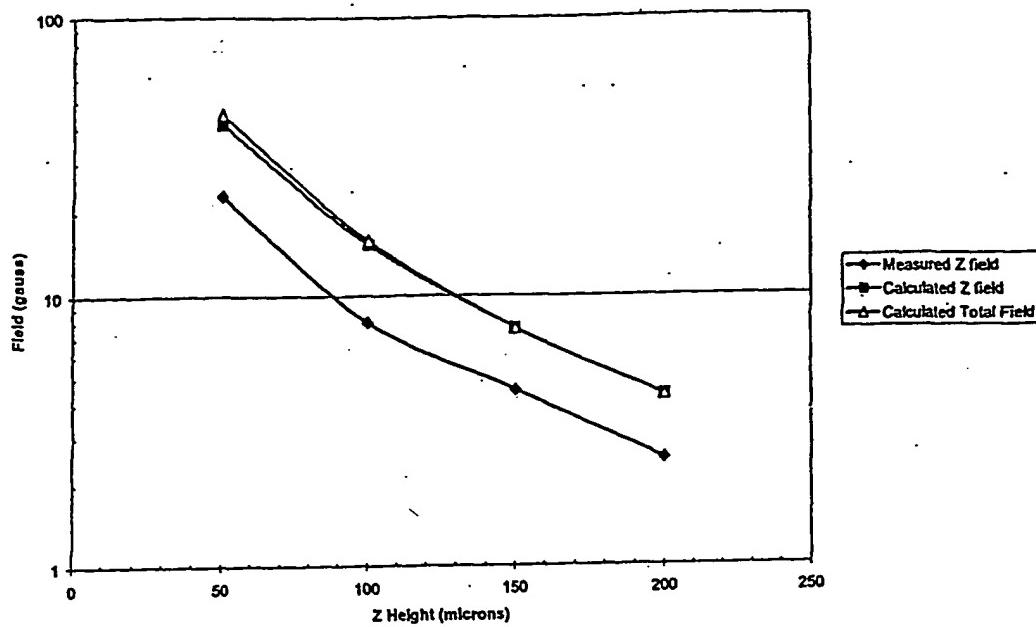


FIG. 21

## 400x50x5 Device

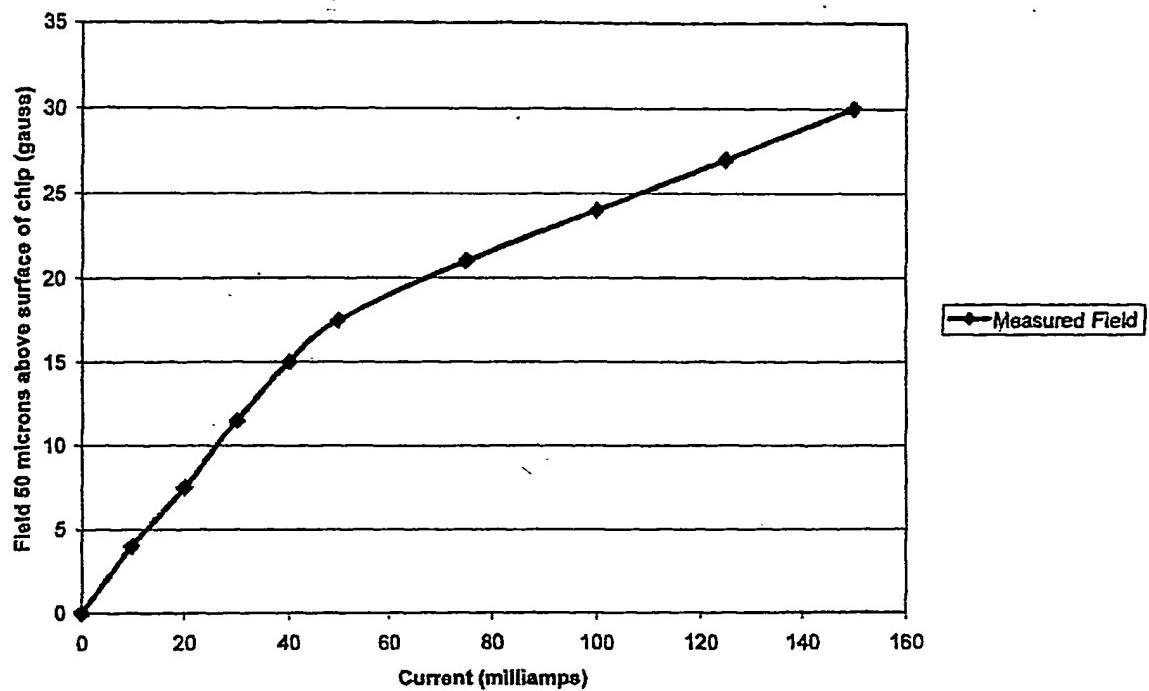


Fig. 22

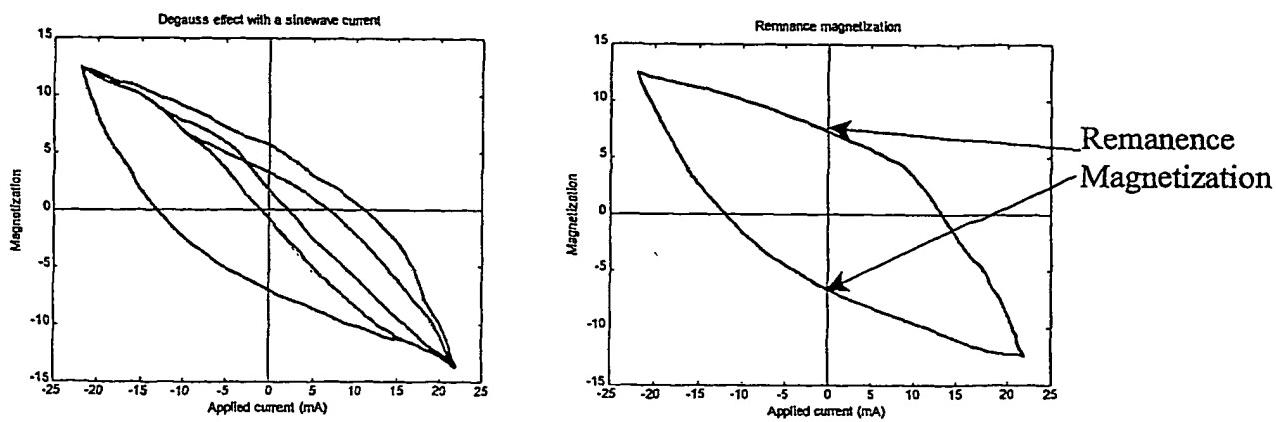


Fig. 23

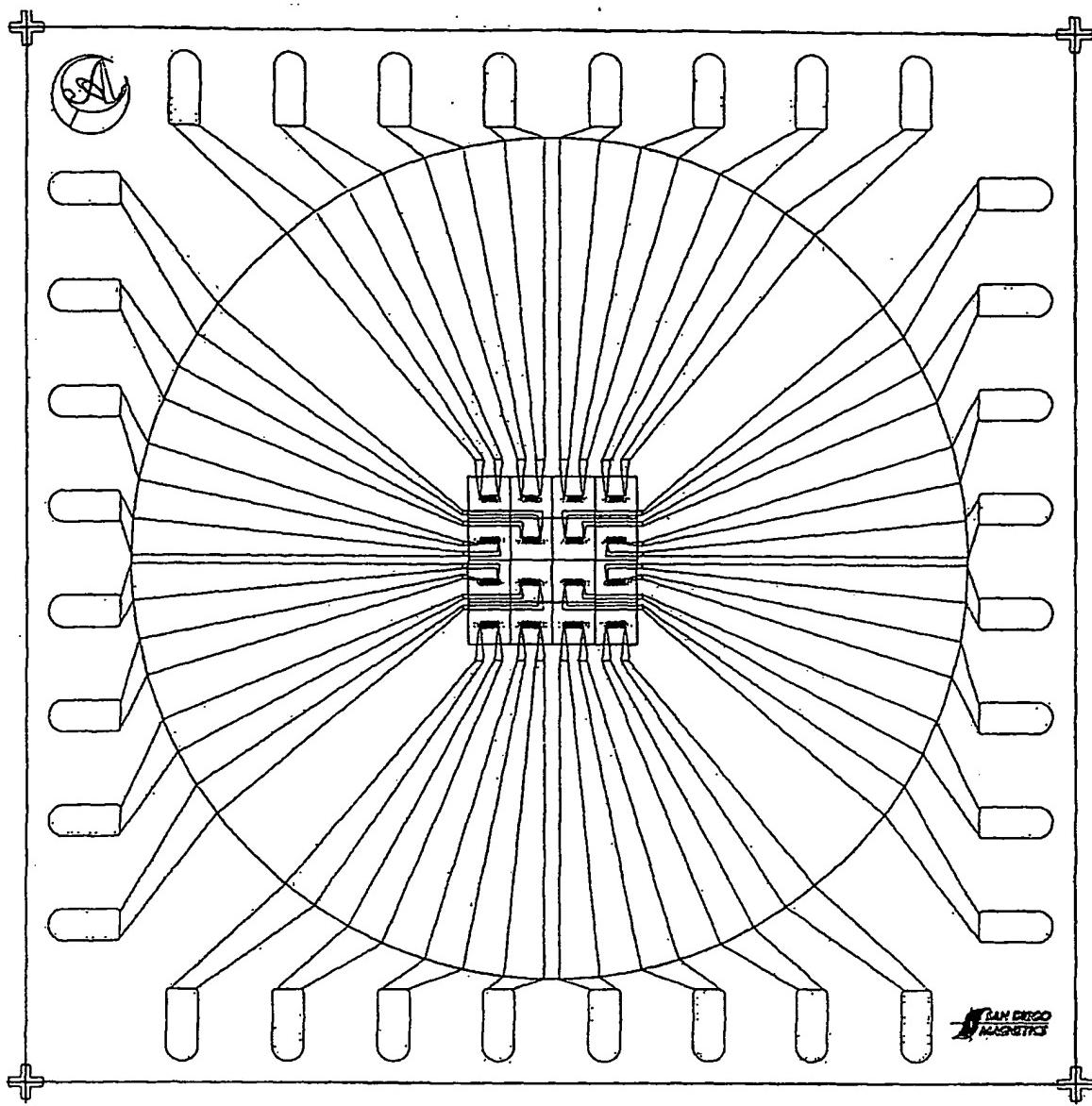


Fig. 24 (A)

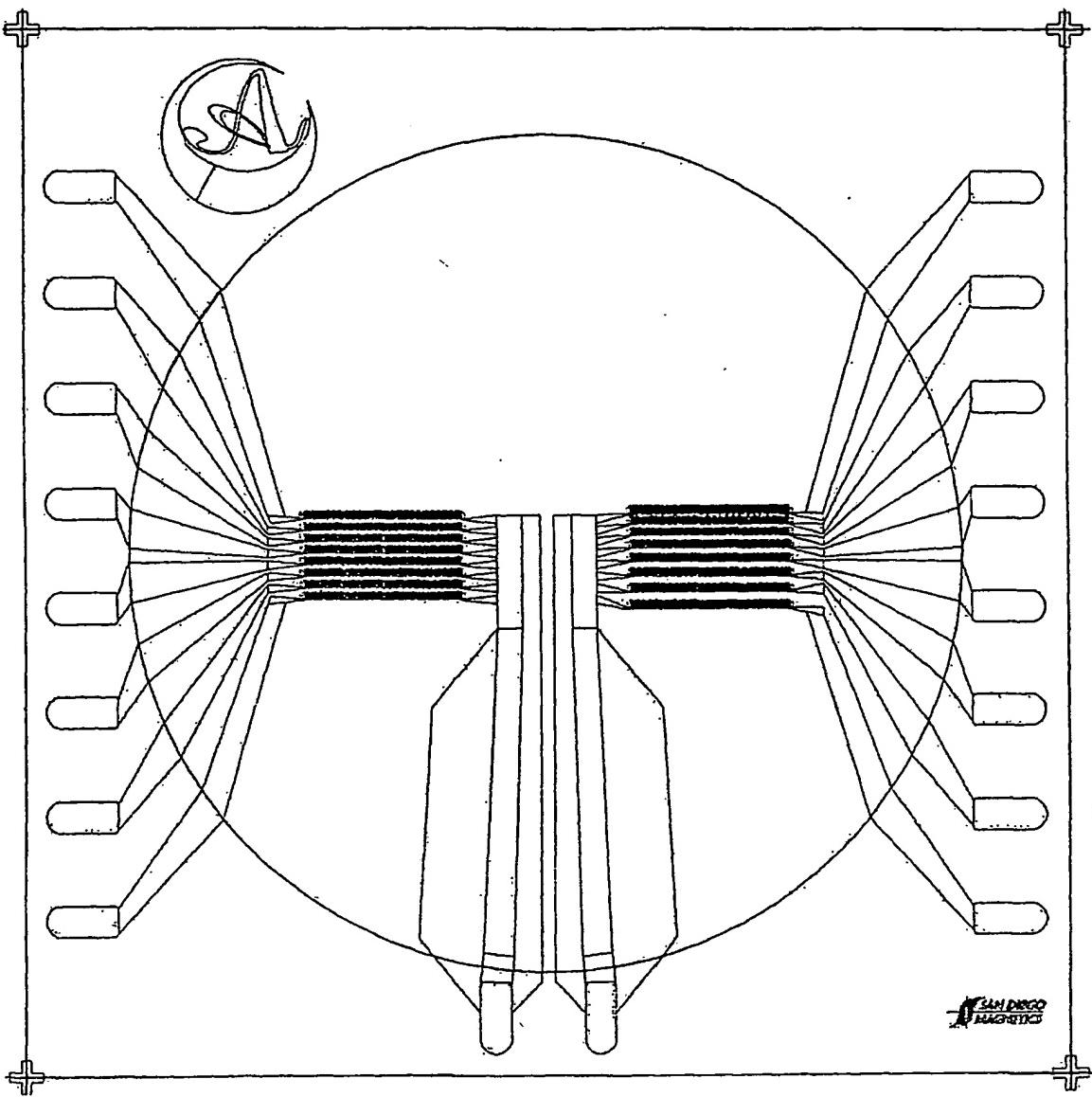


Fig. 24 (B)

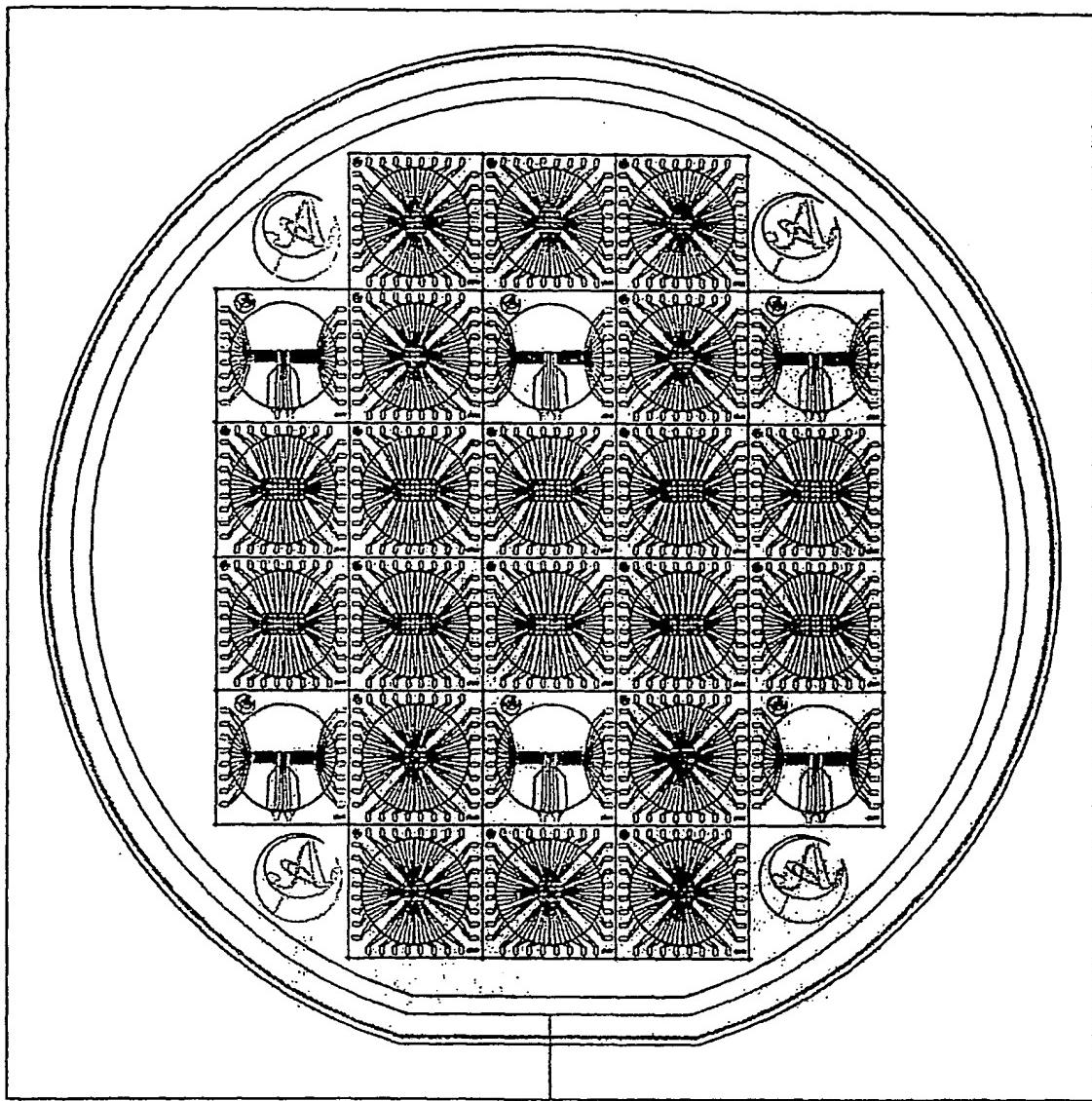


Fig. 25

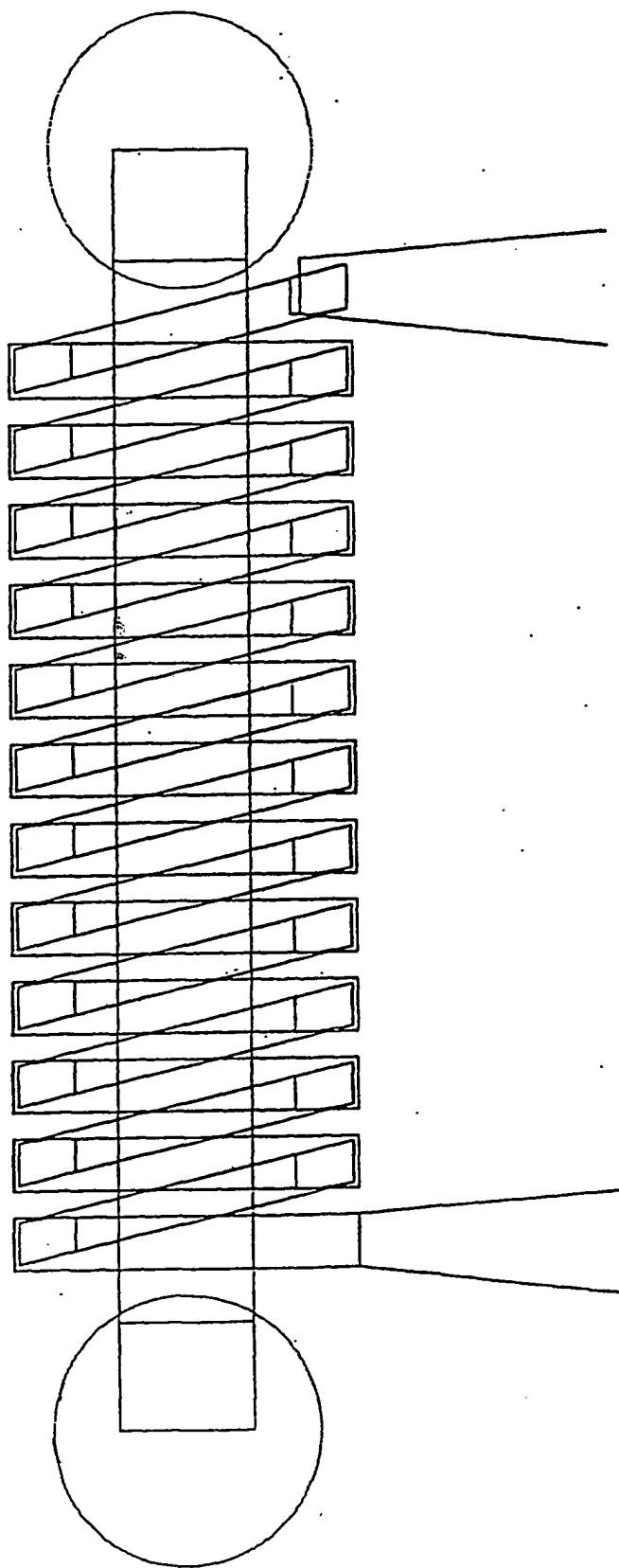


FIG. 26

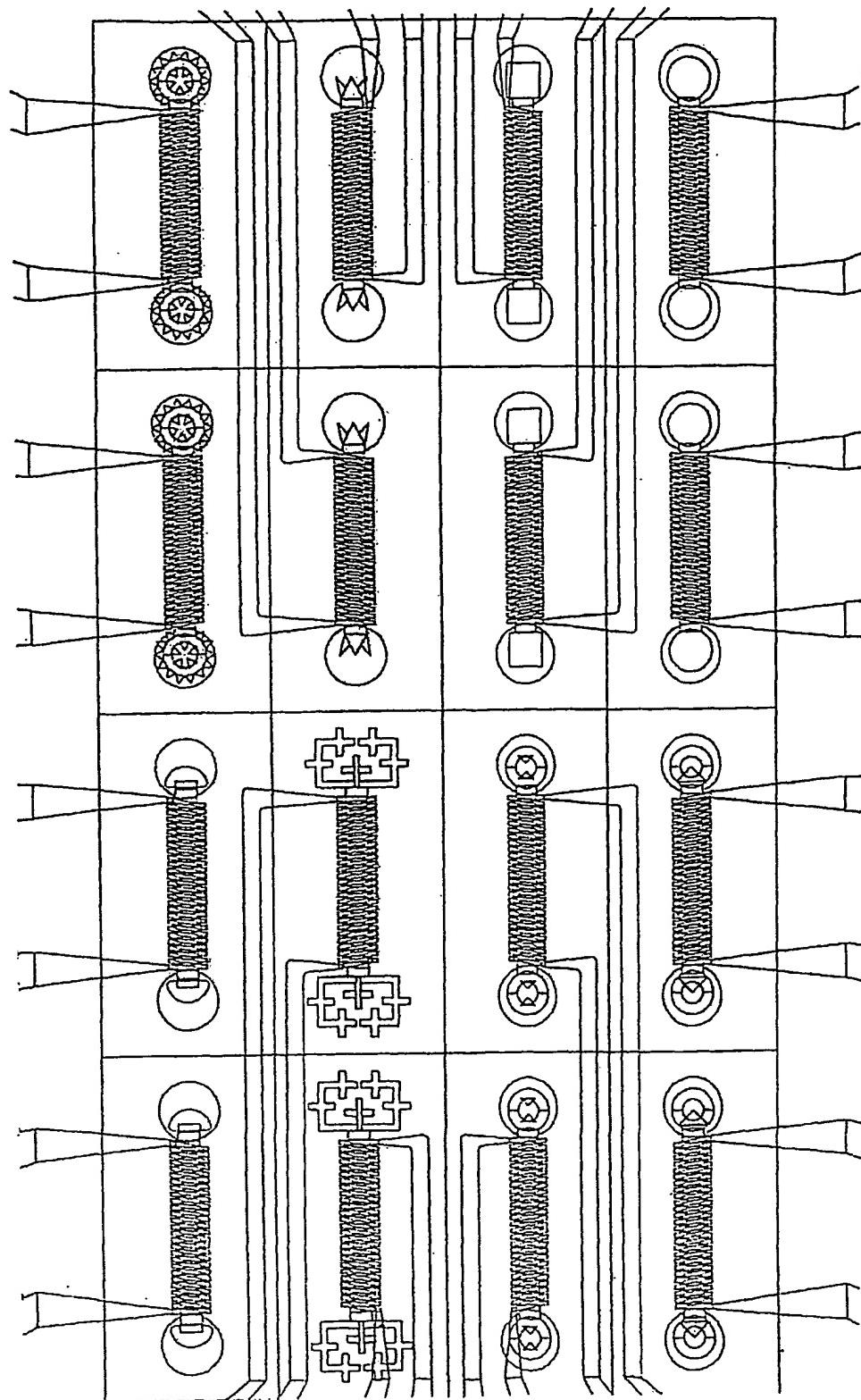


FIG. 27

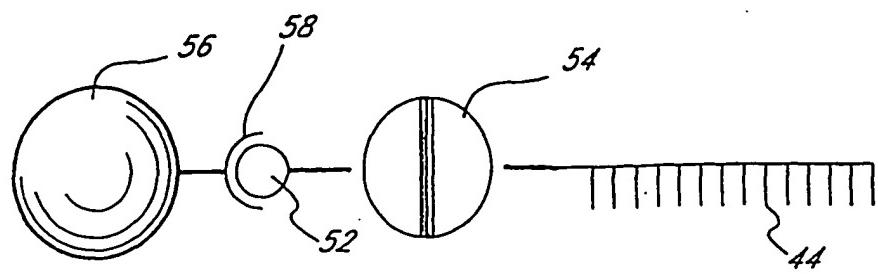


FIG. 28

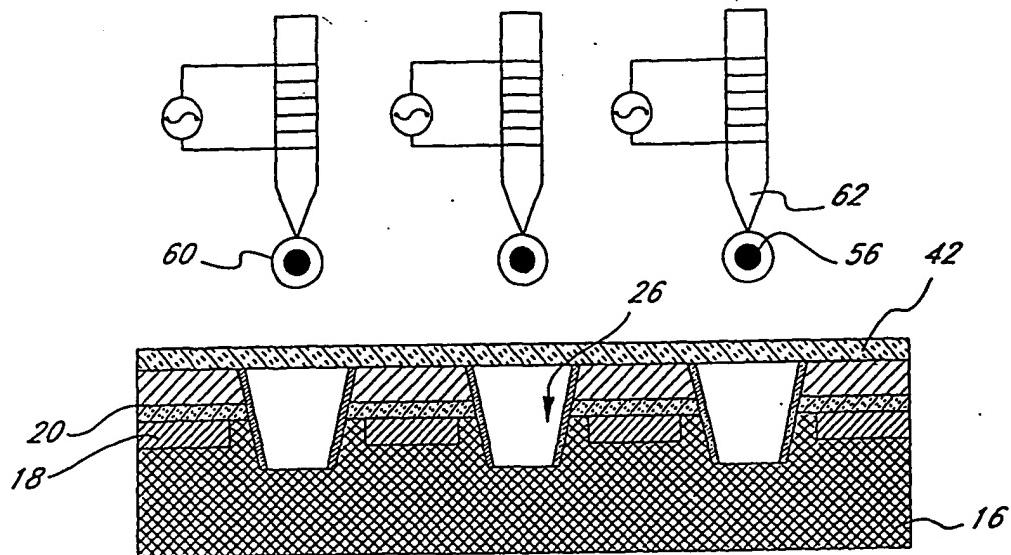


FIG. 29

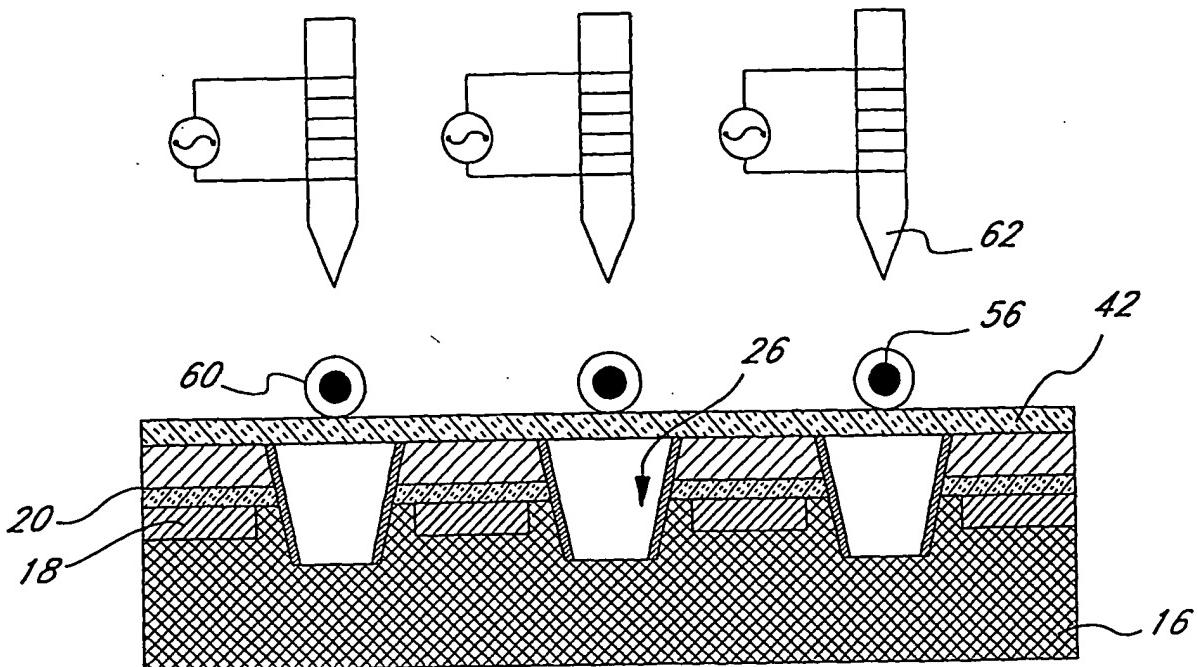


FIG. 30

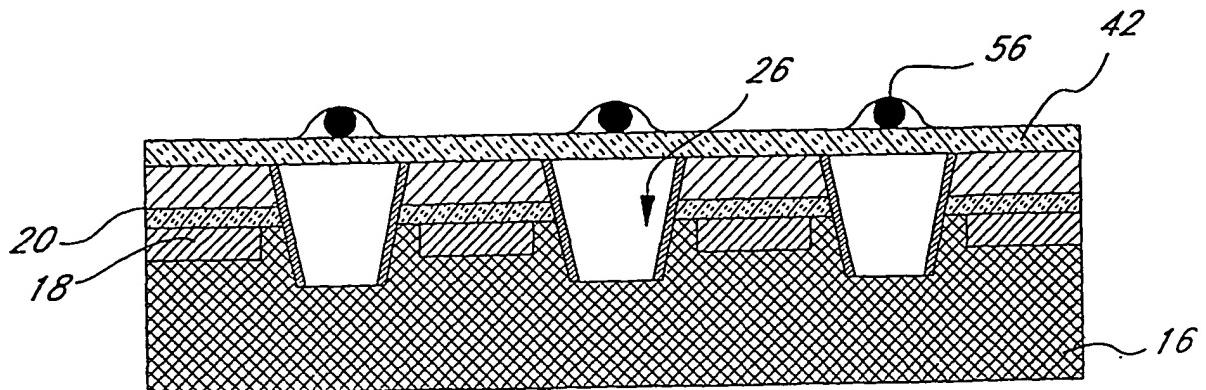


FIG. 31

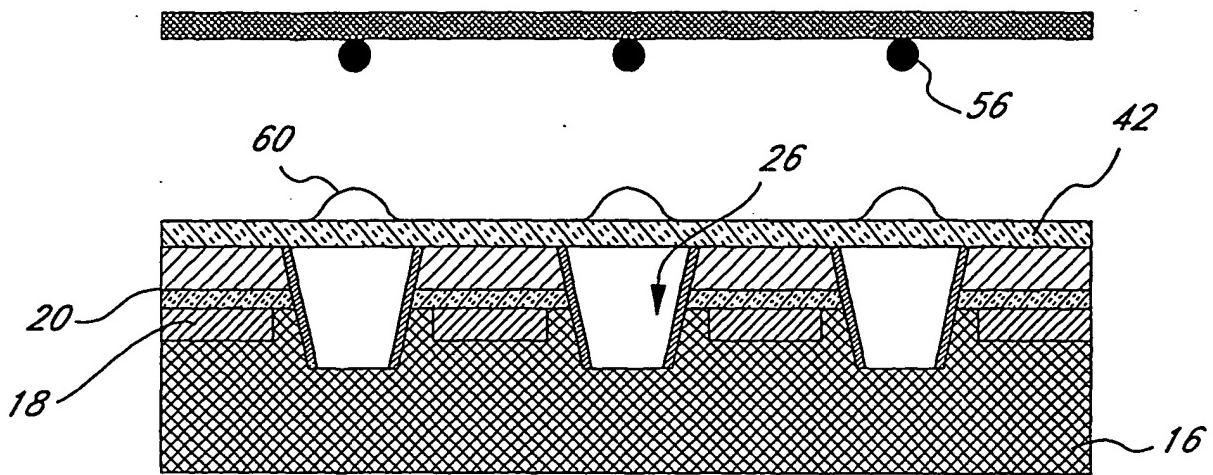


FIG. 32

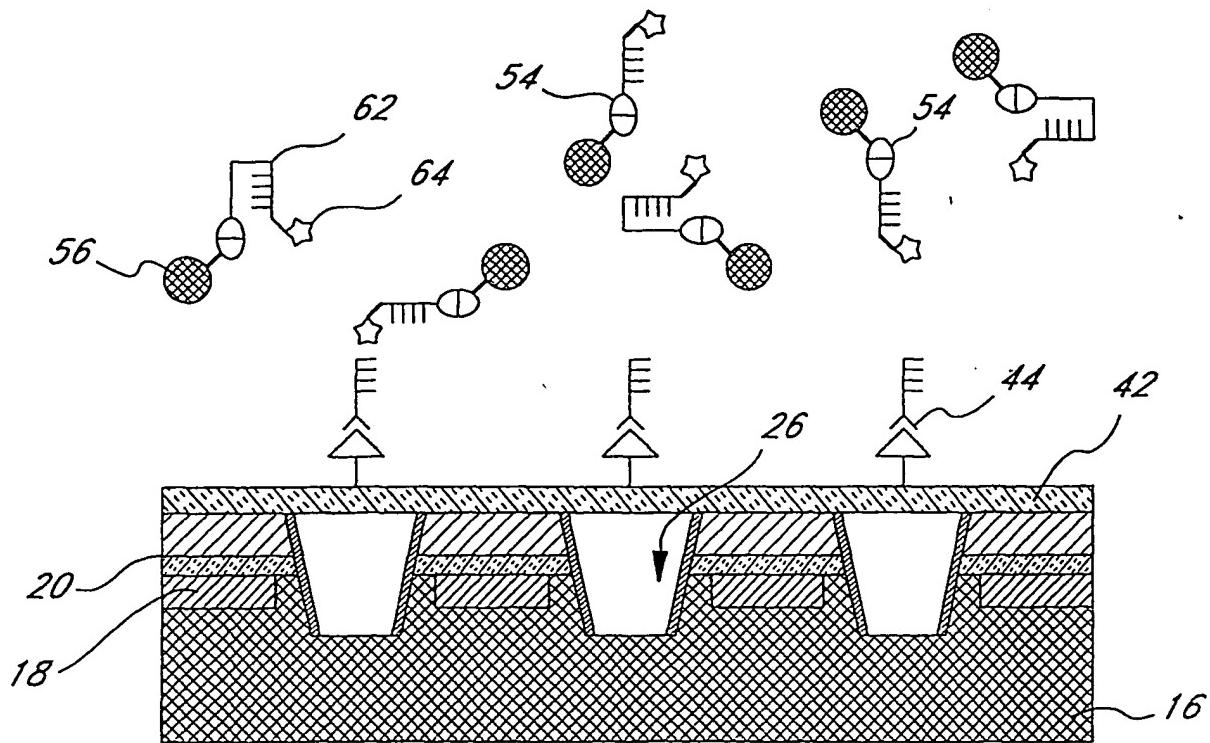


FIG. 33

FIG. 35

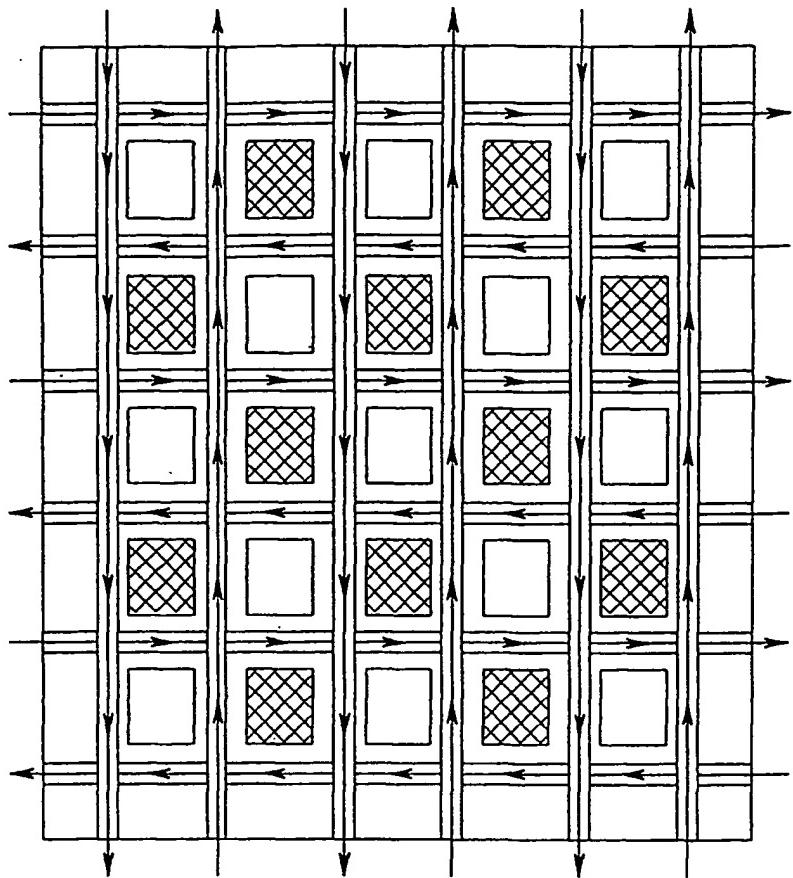


FIG. 34

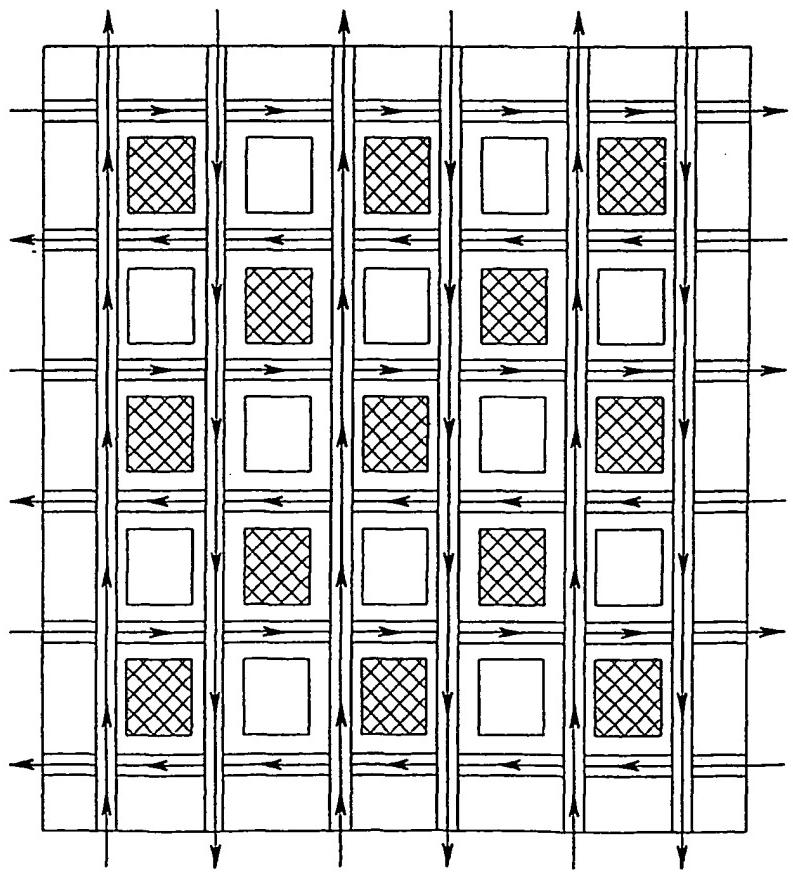


FIG. 36

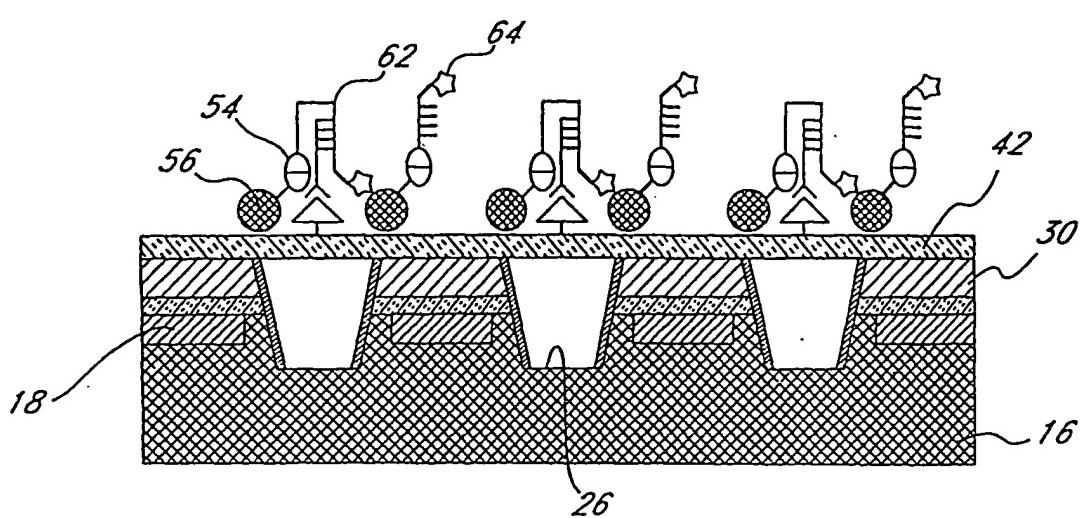
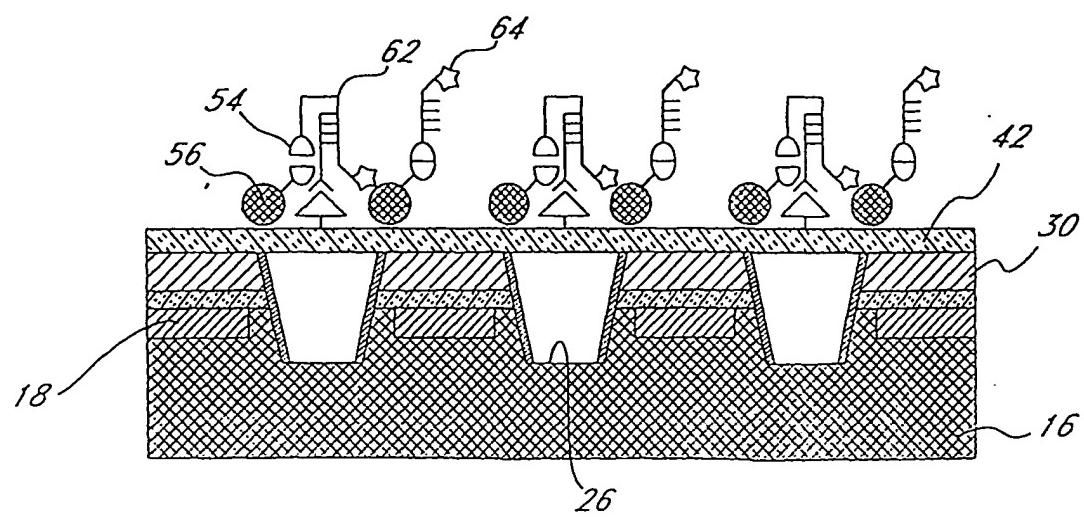


FIG. 37



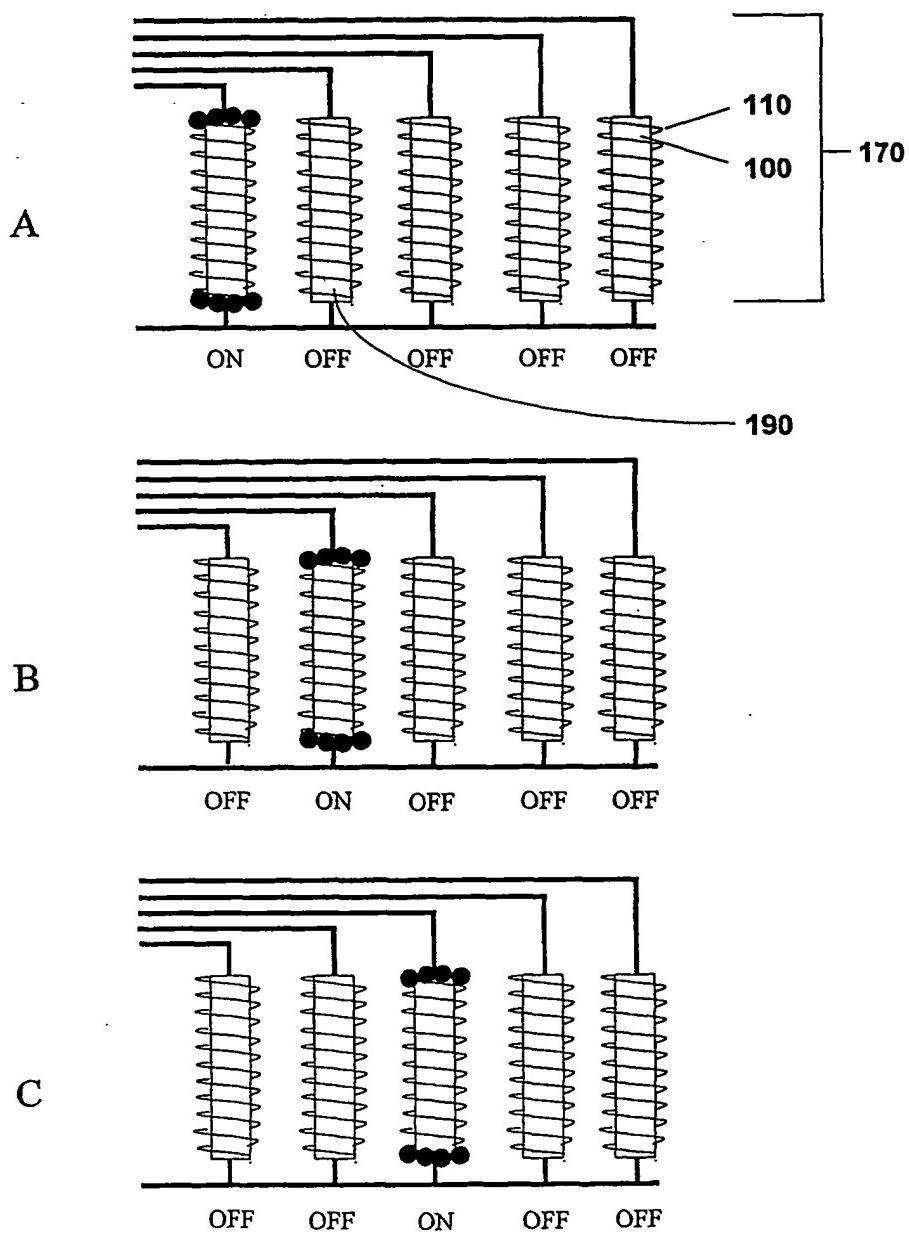
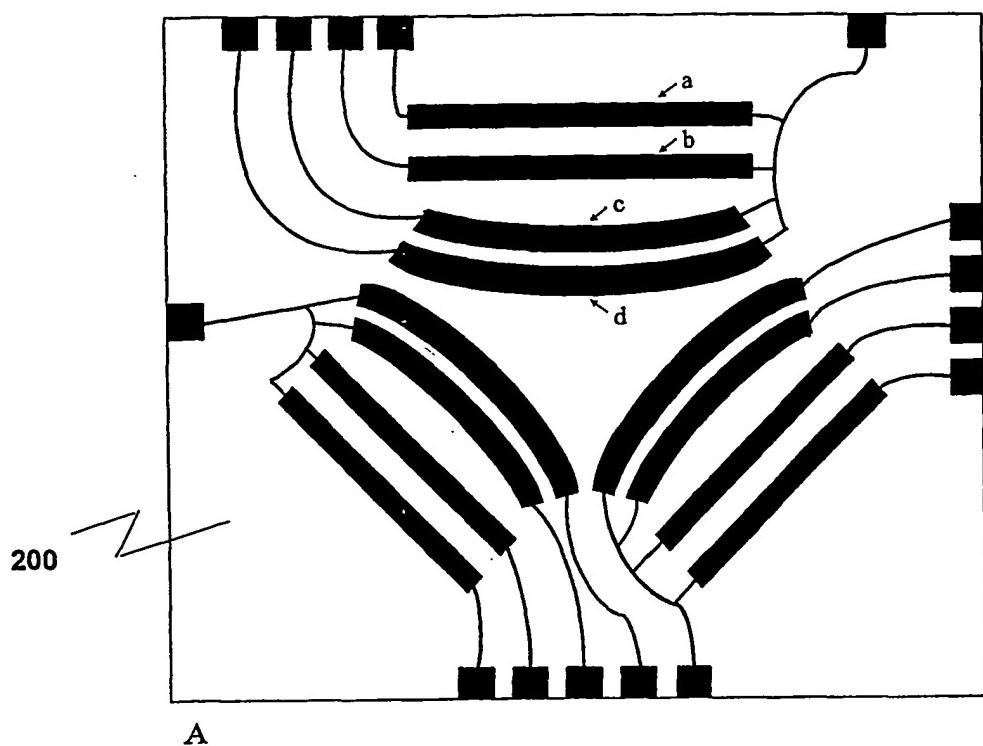
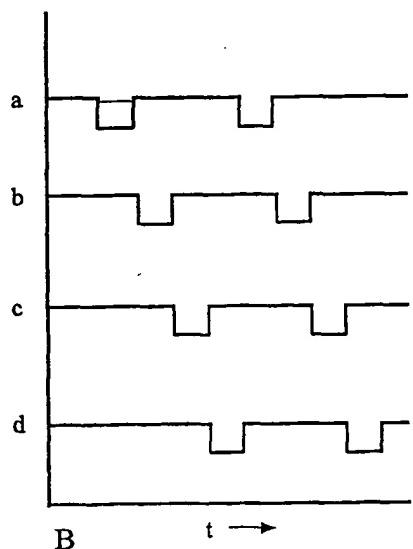


FIG. 38



A



B

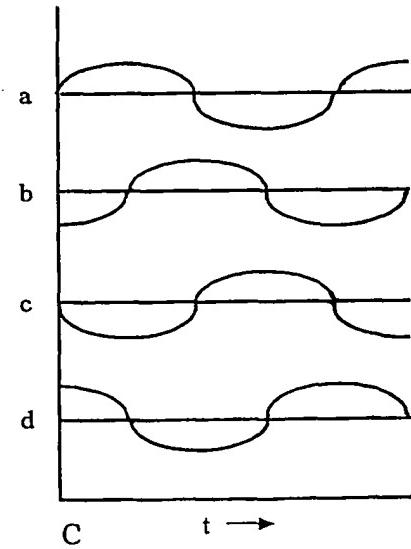


FIG. 39A

32/33

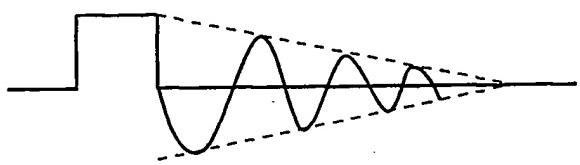


FIG. 39D

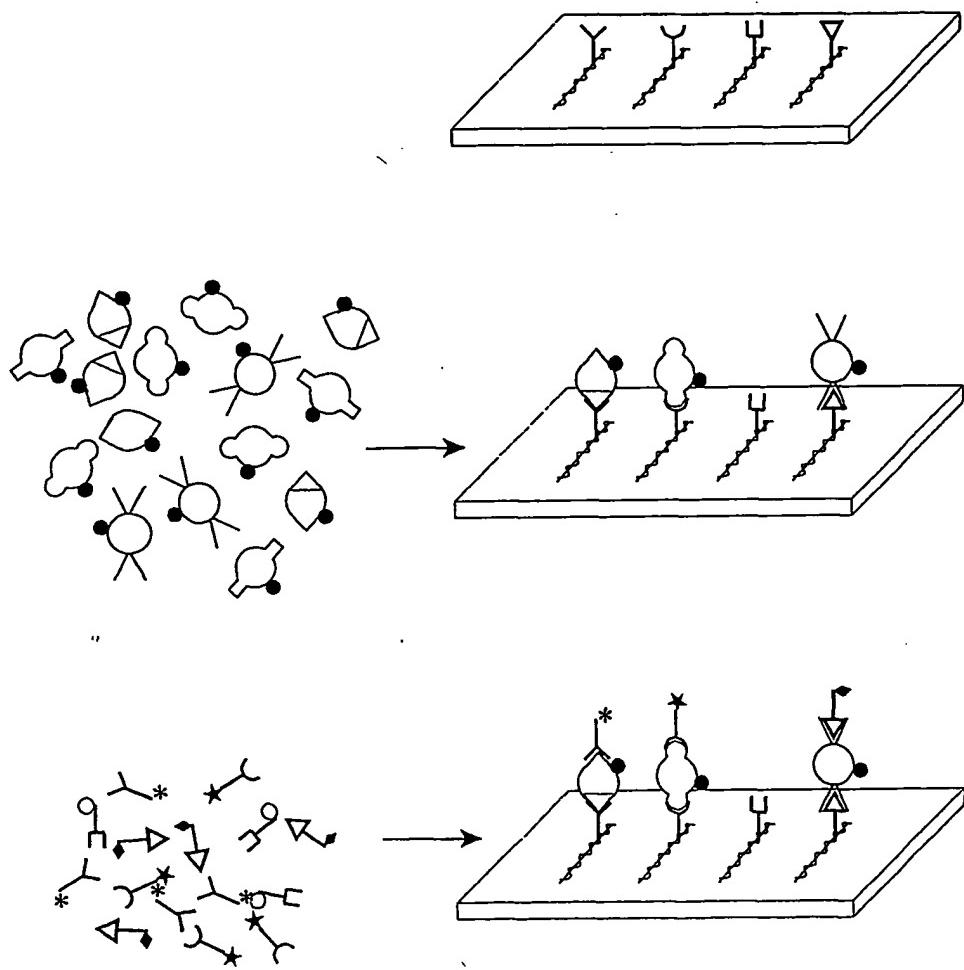


FIG. 40